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# Hardware Based Packet Classification For High Speed Internet Routers

**performance improvement of hardware-based packet ...** - performance improvement of hardware-based packet classification algorithm yaw-chung chen1, pi-chung wang 2, chun-liang lee2, and chia-tai chan 1 department of computer science and information ... **packet classification algorithms: from theory to practice** - packet classification is far from the worst-case scenario. based on the comparison of two representative algorithms with three types of real-life rule sets, we summarize types of solutions of existing work. • a novel algorithm: in this paper, we proposed a novel packet classification algorithm by combining the **block permutations in boolean space to minimize tcam for ...** - packet classification has been used as a basic building block in many network applications such as quality of service (qos), flow-based routing, firewalls, and network address translation (nat) [1][2]. in packet classification, information is extracted from the packet header and compared against a **three different designs for packet classification - wseas** - called packet classification and is based on an arbitrary number of packet header fields. the role of packet classification is important in special services such as vpns, firewalls and differentiated services, and influence wire-speed routing. since the packet classification problem is naturally difficult and is **building balanced search tree based on layered decision ...** - there are many hardware based search engines for packet classification. in this section, we will introduce some search engines for ternary content addressable memory (tcam) or field programmable gate array (fpga). tcam is a specific hardware in which each entry can store a ternary string containing 0, 1 or \* (wildcard). **algorithms for packet classification - stanford university** - packet classification. all packets destined to the set of addresses described by a common prefix may be considered to be part of the same flow. the address of the next hop where the packet should be forwarded to is the associated action. the length of the prefix defines the priority of the rule. 2 performance metrics for classification ... **high-performance packet classification on gpu** - both hardware and software platforms are harnessed in ... based packet classifiers suffer the constraint of the limited on-chip resources, and will undergo long processing latency when using external memory. software-based routers have also been ... **high-performance packet classification on gpu ptree: a system for flexible, efficient packet ...** - packet classification engine as described in [8]. 1.1 background packet classification has been an active research area for many years. many modern operating systems and networking devices include capabilities for packet classification, usually for the purpose of enforcing security, routing or other policies. **tree-based minimization of tcam entries for packet ...** - a) hardware-based solutions: the basic idea of hardware-based solutions is to modify tcam circuits and architecture [2], [4]–[7]. for example, van lunteren et al. proposed a method of adding comparators at each entry to better accommodate range matching in packet classifiers [6]. while this allows to use tcams more efficiently, any solution ... **424 ieee transactions on parallel and distributed systems ...** - packet is the decision of the first (i.e., highest priority) rule that the packet matches. table 1 shows an example classifier of three rules. the format of these rules is based upon the format used in access control lists on cisco routers. 1.1 motivation there are two types of packet classification schemes: software based and hardware based ... **international journal of computer trends and technology ...** - design and implementation of an energy-efficient packet classification hardware accelerator (classifier) that can relieve a network processor's processing engines of the difficult and power hungry networking task of packet classification. pre-cutting scheme only requires an internal or root node to **high-performance classification algorithm for many-core ...** - packet classification is crucial for the internet to provide more value-added services and guaranteed quality of service. besides hardware-based solutions, many software-based classification algorithms have been proposed. however, classifying at 10gbps speed or higher is a challenging problem and it is still one of the **efficient packet classification on fpgas also targeting at ...** - packet classification and increases in the link rate, multi-field based packet classification has become a fundamental challenge for high-speed designs. of course, a software-oriented solution cannot satisfy the required processing speeds. hence, efficient hardware implementations using asic devices or fpgas [22] **research article hardware accelerators targeting a novel ...** - hardware accelerators targeting a novel group based packet classification algorithm o.ahmed,seibi,andgewal school of engineering and computer science, university of guelph, guelph, on ...

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